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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/826,096	04/05/2001	Biing-Seng Wu	4006-117	2725
75	90 03/28/2006		EXAM	INER
LOWE HAUPTMAN GOPSTEIN GILMAN & BERNER, LLP			PATEL, NITIN	
Suite 310		•		
1700 Diagonal Road			ART UNIT	PAPER NUMBER
Alexandria VA 22314			2620	

DATE MAILED: 03/28/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	09/826,096	WU ET AL.				
Office Action Summary	Examiner	Art Unit				
	Nitin Patel	2629				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.  - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.  - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).						
Status						
<ol> <li>Responsive to communication(s) filed on 11 January 2006.</li> <li>This action is FINAL. 2b) This action is non-final.</li> <li>Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.</li> </ol>						
Disposition of Claims						
<ul> <li>4) ☐ Claim(s) 1-12 is/are pending in the application.</li> <li>4a) Of the above claim(s) is/are withdrawn from consideration.</li> <li>5) ☐ Claim(s) is/are allowed.</li> <li>6) ☐ Claim(s) 1-12 is/are rejected.</li> <li>7) ☐ Claim(s) is/are objected to.</li> <li>8) ☐ Claim(s) are subject to restriction and/or election requirement.</li> </ul>						
Application Papers						
<ul> <li>9) The specification is objected to by the Examiner.</li> <li>10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.  Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).</li> <li>11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.</li> </ul>						
Priority under 35 U.S.C. § 119						
<ul> <li>12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority documents have been received.</li> <li>2. Certified copies of the priority documents have been received in Application No</li> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* See the attached detailed Office action for a list of the certified copies not received.</li> </ul>						
Attachment(s)  1) Notice of References Cited (PTO-892)  2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	4) Interview Summary ( Paper No(s)/Mail Da	te				
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  Paper No(s)/Mail Date  5) Notice of Informal Patent Application (PTO-152)  6) Other:						

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### **DETAILED ACTION**

## Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 1-12 are rejected under 35 U.S.C. 102(b) based upon a public use or sale of the invention in view of Tomes et al., (U.S. Patent No. 5,278,086)

As per claim 1,7,10 Tomes shows a scan line circuit that solves screen flicker, imperfect exposure junctions and inhomogeneous brightness in the TFT-LCD, which includes a plurality of TFTs disposed in an array, each array element having a TFT, and a plurality of perpendicular scan lines and data lines, each scan line and data line connecting to a gate and source of a TFT, respectively, with the drain of the TFT connecting to a liquid crystal capacitor and a storage capacitor (In fig.2 and 3 scanning lines 25,23,27 and capacitor 3), wherein the scan line circuit comprising: a gate voltage deformation device( element 33,37,35,39 in fig.2), which connects between the gate of the first TFT(as shown in fig.2) and a input terminal of the scan line to deform the gate input voltage waveform connected to the scan line circuit.

As per claims 2,8,11 Tomes teaches the gate voltage deformation device comprises a resistor (in fig.2 element 33).

As per claim 3,9 Tomes also teaches the resistance of the resistor is in the range between 10 .OMEGA./sq and 100 .OMEGA./sq(in col.4 lines 28-30).

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As per claim 4, Tomes shows wherein the gate voltage deformation device comprises an ITO thin film (in col.3 lines 57-67).

As per claims 5,12 Tomes shows the gate voltage deformation device comprises a TFT with source/gate connection (In fig.2 and 3).

As per claim 6, Tomes teaches wherein the scan line is a metal wire (in col.4 lines 10-15 read as a metal conductive material).

### Response to Arguments

3. Applicant's arguments with respect to claims 1-12 have been considered but are moot in view of the new ground(s) of rejection.

#### Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nitin Patel whose telephone number is 571-272-7677. The examiner can normally be reached on 8:00-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin H. Shalwala can be reached on 571-272-7681. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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March 20, 2006